

Substitute for form 1449A/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	09/964827
		Filing Date	09/25/01
		First Named Inventor	Stephen Melvin Enrique Musoll Mario D. Nemirovsky
		Group Art Unit	2143
		Examiner Name	Kyung H. Shin
Sheet 1 of 3	Attorney Docket Number	MIPS.0183-00-US	

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code 2			
KHS	AA	5708814		Short et al	01/13/1998	
	AB	5724565		Dubey et al	03/03/1998	
	AC	6169745		Liu et al	01/02/2001	
	AD	6460105		Jones et al	10/01/2002	
	AE	6523109		Stephan G. Meier	02/18/2003	
	AF	6535905		Kalafatis et al	03/18/2003	
	AG	2001/0004755		Levy et al	06/21/2001	
	AH	2001/005253		Hiroshi Komatsu	06/28/2001	
	AI	2002/0049964		Takayama et al	04/25/2002	
	AJ	2002/0071393		Enrique Musoll	06/13/2002	
	AK	2002/0083173		Musoll et al	06/27/2002	
✓	AL	09/608750		Nemirovsky et al	Not Published	

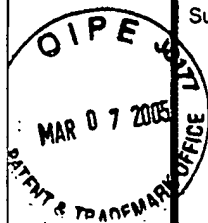
FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No. <sup>1</sup>	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number <sup>4</sup>	Kind Code 5			
KHS	BA	WO 03/005645A2		Sampath et al.	01-16-2003	

Examiner Signature	<i>Kyung H. Shin</i>	Date Considered	9-19-2005
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OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
KHS	CA	MELVIN ET AL, "Extended Instruction Set for a Packet Processing Applications:, July 5, 2001, Disclosure Document #496559, USPTO	
	CB	MUSOLL ET AL, "Mechanism for Allowing a Limited Packet Head and/or Tail Growth Without Moving the Packet to a Different Memory Location," April 16, 2001, Disclosure Document #492429, USPTO	
	CC	MUSOLL ET AL, "Mechanism to Activate a Context When No Stream is Running in a Multi-Streaming Processing Core", April 16, 2001, Disclosure Document #492431, USPTO	
	CD	MUSOLL, ENRIQUE, "Functional Validation of a Packet Management Unit:, May 18, 2001, Disclosure Document #494011, USPTO	
	CE	SAMPATH ET AL, "Mechanism To Un-Speculatively Pre-Fetch Instructions From the Thread Associated to a Packet:, July 5, 2001, Disclosure Document #491527, USPTO	
	CF	YAMAMOTO, WAYNE. <i>An Analysis of Multistreamed, Superscalar Processor Architectures</i> . University of California Santa Barbara Dissertation. December 1995. Santa Barbara, US.	
	CG	YAMAMOTO et al. "Increasing Superscalar Performance Through Multistreaming." <i>Parallel Architectures and Compilation Techniques (PACT '95)</i> . 1995	
	CH	The PowerPC Architecture: A Specification for a New Family of RISC Processors. 2 <sup>nd</sup> Ed. May 1994. pp. 70-72. Morgan Kaufmann. San Francisco, US.	
	CI	MC68020 32-Bit Microprocessor User's Manual. 3 <sup>rd</sup> Ed.. 1989. pp. 3-125, 3-126, and 3-127. Prentice Hall, NJ, US.	
	CJ	MUSOLL ET AL, "Mechanism to Overflow Packets to a Software Controlled Memory When They Do Not Fit into a Hardware Controlled Memory, July 3, 2001, Disclosure Document #496391, USPTO	
✓	CK	PAI, VIJAY ET AL. "An Evaluation of Memory Consistency Models for Shared-Memory Systems with ILP Processors." <i>Proceedings of ASPLOS-VII</i> , Oct. 1996: pp. 12-23, ACM, Inc.	

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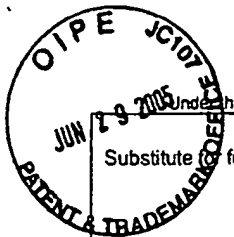
<b>OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
KHS	CL	POTEL, M. J. "Real-Time Playback in Animation Systems." <i>Proceedings of the 4<sup>th</sup> Annual Conference on Computer Graphics and Interactive Techniques</i> . 1977. pp. 72-77. San Jose, CA, US.	
	CM	ARM Architecture Reference Manual. 1996. pp. 3-41, 3-42, 3-43, 3-67, and 3-68. Prentice Hall, NJ, US.	
	CN	ESA/390 Principles of Operation. IBM Online Publications Center Reference Number SA22-7201-08. Table of Contents and paras. 7.5.31 and 7.5.70. IBM Corporation. Boulder, CO, US.	
	CO	MC88110 Second Generation RISC Microprocessor User's Manual. 1991. pp. 10-66, 10-67, and 10-71. Motorola, Inc.	
	CP	DIEFENDORFF, KEITH ET AL. "Organization of the Motorola 88110 Superscalar RISC Microprocessor." <i>IEEE Journal of Microelectronics</i> . April 1992. pp. 40-63. Vol. 12, No. 2. IEEE. New York, NY, US.	
	CQ	KANE, GERRY. <i>PA-RISC 2.0 Architecture</i> . 1996, pp. 7-106 and 7-107. Prentice Hall. NJ, US.	
	CR	DIEFENDORFF, KEITH ET AL. "Altivec Extension to PowerPC Accelerates Media Processing." <i>IEEE Journal of Microelectronics</i> . Vol. 20, No. 2 (2000): pp. 85-95	
	CS	GRUNEWALD, WINFRIED ET AL. "Towards Extremely Fast Context Switching in a Block Multithreaded Processor." <i>Proceedings of EUROMICRO 22</i> , 1996. pp. 592-599	
	CT	BRADFORD, JEFFREY ET AL. "Efficient Synchronization for Multithreaded Processors." <i>Workshop on Multithreaded Execution, Architecture, and Compilation</i> . Jan-Feb 1998. pp. 1-4	
	CU	YOAZ ET AL. "Speculation Techniques for Improving Load Related Instruction Scheduling." 1999. Pages 42-53. IEEE.	
	CV	KESSLER, R. E. "The Alpha 21264 Microprocessor: Out-of-Order Execution at 600 MHz." August 1998.	
	CW	DONALDSON ET AL. "DISC: Dynamic Instruction Stream Computer, An Evaluation of Performance." 26 <sup>th</sup> <i>Hawaii Conference on Systems Sciences</i> . Vol. 1. 1993. Pages 448-456.	
	CX	MUSOLL ET AL. "Hardware Algorithm for Allocating and De-Allocating Consecutive Blocks of Memory," April 3, 2001. Disclosure Document #491557, USPTO	
✓	CY	NEMIROVSKY ET AL. "DISC: Dynamic Instruction Stream Computer." ACM. 1991. Pages 163-171	

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Sheet	1	of	1
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		Number-Kind Code <sup>2</sup> (if known)			
KHS		4200927	04-29-1980	Huges et al.	
		6151644	11-21-2000	Chun-Chu Archie Wu	
		6453360	09-17-2002	Muller et al.	
		20010052053	12-13-2001	Nemirovsky et al.	
		20010043610	11-22-2001	Nemirovsky et al.	
		20020016883	02-07-2002	Musoll et al.	
		20020054603	05-09-2002	Musoll et al.	
		20040213251	10-28-2004	Tran et al.	
		20040172504	09-02-2004	Balazich et al.	
		20040015598	01-22-2004	Jin-Fun, Lin	
		09/602,279	NA	Nemirovsky et al.	
		5559970	09-24-1996	Vinod Sharma	
		6614796	09-02-2003	Black et al.	

**NON PATENT LITERATURE DOCUMENTS**

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KHS	AA	MUSOLL ET AL. Mechanism to Prevent the Download of a Packet with Pending Writes Affecting Its Data. April 11, 2001. Disclosure Document #492430, USPTO	
↓	AB	UNGERER ET AL. A Survey of Processors with Explicit Multithreading. ACM Computing Surveys, Vol., 35, No. 1. March 2003. Pages 29-63.	

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